

## FEATURES

- Loss from  $-5\text{dB}$  to  $-40\text{dB}$  at for 10 and 28 Gbps
- Keysight ADS Design Guide and Simulation Examples included. Matlab Q3 2015
- Causal/Passive high quality S-parameters included
- Superb signal integrity
  - Low weave impact
  - Pristine launch design
- Optional matched low skew cables available
- Optional 3dB, 6dB, 10dB, and 20dB attenuator pads available

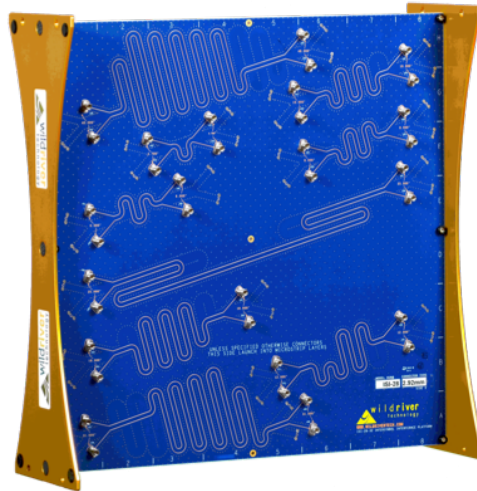
## APPLICATIONS

- SERDES Receiver Tolerance Testing
- Communication Channel Optimization using DFE, FFE, CTLE, and Gain Stage Elements
- Jitter Analysis and Benchmarking
- Related Products —  
XTLK-28, PATHO-I (Q3 2015)

## ISI-28/32/40 Loss Modeling Platform

### 6-32 Gbps Advanced SERDES Testing - Channel Optimization – Tolerance Testing

The **ISI-28**, **ISI-32**, and **ISI-40** represent the next generation of Wild River Technology platforms with controlled amounts of ISI for creating jitter to 32 Gbps. True Intersymbol Interference (ISI) is created by loss generated with dielectric and skin effect only. Group delay variation, an important loss consideration for data rates exceeding 10 Gbps, has been minimized with special layout techniques, pristine connector launches, and the use of controlled weave dielectric material. The ISI Platform is composed of 9 microstrip and 9 stripline differential pairs of various lengths. Ground vias surrounding each structure preserve jitter spectral purity by suppressing unwanted resonance.



Open to Closed Eye for 6-32Gbps In Minutes

Combine with XTLK-32 for LOSS-Crosstalk Testing

Pre-Layout, Pre-Silicon EDA Capable

The **ISI-28/32/40** platform kits include the assembled and tested platform, a ruggedized carrying case and patent pending stands. The stands provide measurement access on any orientation.