



HIGH SPEED SIGNAL INTEGRITY SOLUTIONS



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DESIGNCON 2025 TECHNICAL PAPER SESSION:

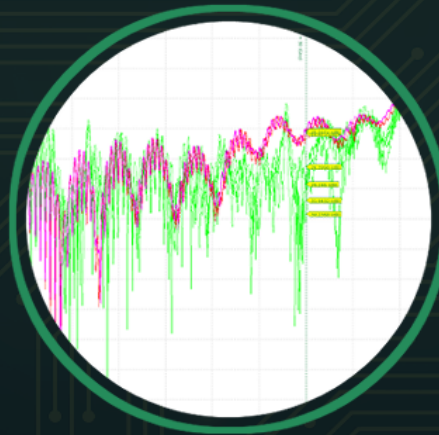
FREE SIGNAL INTEGRITY? HOW UNDERSTANDING ANISOTROPIC MATERIALS AND TOLERANCES COULD INCREASE PERFORMANCE AT 112/224 GBPS & BEYOND

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Wednesday, January 29, 2025
11:15 - 12:00 pm • Ballroom B

DESIGNCON[®] 2025
WHERE THE CHIP MEETS THE BOARD

JAN 28 2025 - JAN 30, 2025
SANTA CLARA, CA